Substitute for form 1449/PTO

TWENTY-FIRST SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known	
Application Number	10/684,348
Filing Date	October 10, 2003
First Named Inventor	Kevin D. Kissell
Art Unit	2195
Examiner Name	Zhe, Meng Yao
Attorney Docket Number	1778.3310000 (0189.00US)

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	US1	5,295,265	03-15-1994	Ducateau *et al.*	
	US2	5,428,754	06-27-1995	Baldwin	
	US3	5,835,748	11-10-1998	Orenstein *et al.*	
	US4	6,240,531	05-29-2001	Spilo *et al.*	
	US5	6,253,306	06-26-2001	Ben-Meir *et al.*	
	US6	7,386,636	06-10-2008	Day *et al.*	
	US7				
	US8				
	US9				
	US10				
	US11				
	US12				
	US13				
	US14				
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	US16				
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document Country Code[3] Number[4] Kind Code[5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]
	FP1					
	FP2					
	FP3					
	FP4					
	FP5					
	FP6					
	FP7					
	FP8					
	FP9					

Examiner Signature		Date Considered	

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Sheet	1	of	1	Attorney Docket Number	1778.3310000 (0189.00US)

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T[2]
	NPL1	HAGGANDER, D. *et al.*, "Optimizing Dynamic Memory Management in a Multithreaded Application Executing on a Multiprocessor," *Proceedings of the International Conference on Parallel Processing*, August 10-14, 1998, pp. 262-269.	
	NPL2	KISSELL, K.D., U.S. Patent Application No. 12/605,201, filed October 23, 2009, entitled "Apparatus, Method, and Instruction for Initiation of Concurrent Instruction Streams in a Multithreading Microprocessor, " 34 pages.	
	NPL3	Office Communication, dated July 6, 2009, for U.S. Patent Appl. No. 11/615,960, filed December 23, 2006, 16 pages.	
	NPL4	Office Communication, dated October 16, 2009, for U.S. Patent Appl. No. 11/615,963, filed December 23, 2006, 5 pages.	
	NPL5	Office Communication, dated October 20, 2009, for U.S. Patent Appl. No. 11/615,964, filed December 23, 2006, 5 pages.	
	NPL6		
	NPL7		
	NPL8		
	NPL9		
	NPL10		

Examiner Signature		Date Considered	

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